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PATENT
740756-001400

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of:

Shunpei YAMAZAKI et al.

Application No.: 08/520,079

Filed: August 28, 1995

For: SEMICONDUCTOR CIRCUIT
FOR ELECTRO-OPTICAL DEVICE)
AND METHOD OF
MANUFACTURING THE SAME)

Art Unit: 2815
Examiner: J. Jackson

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on.

SUPPLEMENT TO PRIOR INFORMATION
DISCLOSURE STATEMENT

Commissioner of Patents
Washington, D.C. 20231

May 31, 2001

Dear Sir:

Applicants respectfully submit the attached and request the Examiner to consider the four (4) attached references. An Information Disclosure Statement was filed on May 3, 2001, submitting reference JP 2-61032. A second Information Disclosure Statement was filed on April 26, 2001, submitting the three other Japanese references listed on the attached PTO-1449, for the Examiner's consideration. Applicants hereby submit these same four (4) Japanese references, but with Abstracts for each of the four references as they were received from Applicants.

It is requested that the accompanying Supplemental Information Disclosure Statement be considered and made of record in the above-captioned application. To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner-initialed copy of this form be returned to the undersigned.

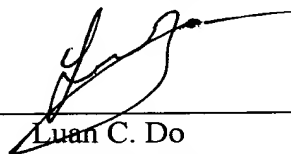
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The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

By



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EJR/LCD:sbs